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	Application Number	10/612,793
	Filing Date	July 2, 2003
	First Named Inventor	Arup Bhattacharyya
	Group Art Unit	2826
	Examiner Name	Thomas Dickey
Sheet 1 of 1	Attorney Docket No: 1303.111US1	

US PATENT DOCUMENTS				
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-6,674,120	01/06/2004	Fujiwara, Ichiro	04/27/2001
	US-6,845,034	01/18/2005	Bhattacharyya, A.	03/11/2003
	US-7,245,535	07/17/2007	McCollum, J., et al.	09/21/2005
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	US-7,339,830	03/04/2008	Bhattacharyya, A.	01/23/2007

OTHER DOCUMENTS – NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ¹
	LEE, S. W, et al., "Low Temperature Poly-Si Thin Film Transistor Fabrication by Metal-Induced Lateral Crystallization". IEEE Electron Device Letters, (April 1996), 160-162			

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant is to place a check mark here if English language Translation is attached